



# POORNIMA

## COLLEGE OF ENGINEERING

### **A report on “Two-Week ISTE STTP on CMOS Mixed Signal and Radio Frequency VLSI Design”**

**Duration (30th January to 04th February 2017)**

On 30<sup>th</sup> January, 2017, **Two-Week ISTE STTP on CMOS Mixed Signal and Radio Frequency VLSI Design** sponsored by National Mission on Education through ICT (NMEICT), funded by MHRD was inaugurated at Poornima College of Engineering, Jaipur.

Inaugural Session witnessed the presence of Dr. Om Prakash Sharma, Director, PCE, Dr. Garima Mathur, HOD (ECE), Dr. Ajay Khunteta (Remote Centre Coordinator) and Mr. Sandeep Gupta (workshop Coordinator) along with 25 Faculties of various Engineering Colleges of Rajasthan.

Dr. Om Prakash Sharma, while addressing the gathering informed that mission of MHRD, Govt. of India is to train teachers so that they are strong enough towards the fundamentals of C-MOS and Mixed Signal Processing and become capable enough to deliver to the students in the class. He further informed that during two weeks from 30th January to 04th February 2017, the course content of the workshop will provide training to faculties on teaching pedagogy to make extensive use of Technology.

He briefed that during 1 week following topics of subject VLSI Design will be covered as follows:

Module 1 Radio Frequency Integrated Circuits (RFIC): Module-A: Fundamental of RFIC design, How RF design alters from traditional analog design. Various commercial protocols, basic Tx-Rx Architecture, Module-B: Various performance parameters, Noise and Module-C: Design of LNA, VCO, Mixer and frequency synthesizer, Power amplifier, High speed I/O (SERDES), layout of RF circuit design.

Module 2 Mixed signal Blocks: Digital VLSI Design: -Algorithmic level b) Architectural level of video processing and Analog Design: Analog front-end electronics (AFE), SAR ADC, Pipelined ADC.

Poornima College of Engineering, Jaipur has been operating as remote centre for IIT MHRD Mission since May, 2012 and organizing various activities as directed by IIT BOMBAY and IIT Kharagpur. The lecture transmission and live interaction takes place through distance mode using the AVIEW technology on the Internet to various coordinating remote centers across the country.

The two week program will be live transmitted from IIT Kharagpur. The faculties involved in the teaching are: Prof. T. K. Bhattacharya, Prof. Indrajit Chakrabarti, and Dr. Mrigank Sharad.

Dr. Ajay Khunteta briefed about RC activities at PCE, Dr. Garima Mathur appealed all the faculties to be regular and active throughout the session.

Approx. twenty five(25) Faculty members from various Engineering Colleges of Rajasthan have registered themselves as participants. Mr. Sandeep Gupta proposed a vote of thanks to all present.



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Department of Electronics & Communication Engineering

(Approved by AICTE, Accredited by NBA)

**TWO WEEK ISTE STTP ON CMOS, MIXED SIGNAL & RADIO FREQUENCY  
VLSI DESIGN**

Under the National Mission on Education through ICT  
(MHRD, Govt. of India)

**Jan 30 - Feb 4, 2017**



Conducted by **IIT Kharagpur**

[www.nmeict.iitkgp.ernet.in](http://www.nmeict.iitkgp.ernet.in)



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